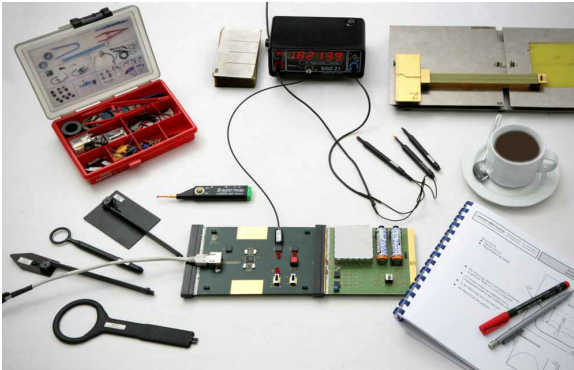


# SF1

## Experimental EMC Seminar - Interference Immunity 1



### Short description

In the Experimental EMC Seminar - Interference Immunity 1, participants will learn methods for analyzing and optimizing the EMC characteristics of circuits and devices. Additionally, participants will focus on detecting and eliminating the causes of interference-immunity problems during burst- or ESD testing.

With the help of numerous practical examples the participants learn the various steps of interference suppression using EMC measuring instruments.

By carrying out a large number of guided experiments on their own, participants are able to test their learned knowledge and gain experience in dealing with measurement technology and troubleshooting techniques.